

CLAIMS:

1. A semiconductor integrated circuit device comprising a logic circuit to perform a predetermined process, a clock generator to supply a clock signal to the logic circuit, and a speed controller to control operating speed of the logic circuit, characterized in that the clock generator changes the frequency of the clock signal by a frequency control signal during a time when the logic circuit is operating, and the speed controller controls the operating speed of the logic circuit in accordance with a change in the clock signal.

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2. A semiconductor integrated circuit device comprising a logic circuit having at least first and second blocks, first and second speed controllers, and a clock generator, characterized in that clock signals of different frequencies are supplied to the first and second blocks, and the first and second speed controllers control the operating speeds of the logic circuit in the blocks in accordance with the clock signals supplied to the respective blocks.

3. A semiconductor integrated circuit device comprising a logic circuit having at least first and second blocks, first and second speed controllers, and a clock generator, characterized in that different supply voltages are supplied to the first and second blocks, and the first and second speed controllers control operating speeds of the logic circuit in the blocks in accordance with the supply voltages supplied to the respective blocks.

4. A semiconductor integrated circuit device comprising a logic circuit having at least first and second blocks and first and second speed controllers, characterized in that at least one of the first and second speed controllers is comprised of a delay detector and a controller, and the delay detector is disposed

5 in the center of the corresponding first or second block.